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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/519,463 03/06/2000 PAT 6,658,581  
*ble*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11-86167 03/29/1999

JAPAN 2000-3449 01/12/2000  
*ble*IF REQUIRED, FOREIGN FILING LICENSE  
GRANTED

\*\* SMALL ENTITY \*\*

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35 USC 119 (a-d) <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature <i>ble</i> Initials	JAPAN	30	36	4

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## TITLE

Timing adjustment of clock signals in a digital circuit

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